

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▾](#)[Search settings](#) | [Sign in](#)[Advanced Search](#)[Web](#) [Show options...](#)

Results 1 - 30 of about 666 for "new opcode"

**[PDF]** Where is the FEEB? The Effectiveness of **Instruction Set** RandomizationFile Format: PDF/Adobe Acrobat - [View as HTML](#)**Instruction Set** Randomization (ISR). 0x3c. 0011 1100. 0x33. 0011 0011. 0x0F. 00001111. Original opcode: Encoding Key: XOR. **New opcode:** ...[www.cse.psu.edu/~mcDaniel/cse544/slides/cse544-feeb-rueda.pdf](#) - Similar

by N Scovarelli - 2005 - Cited by 60 - Related articles

**[PDF]** The RISC-16 **Instruction-Set** ArchitectureFile Format: PDF/Adobe Acrobat - [View](#)Like the MIPS **instruction-set** architecture, by hardware convention, .... causes a new**instruction** word to be read out and a **new opcode** to be sent to the ...[www.ece.umd.edu/courses/enee350H.F2009/F2002-RISC-seq.pdf](#) - Similar

by B Jacob - Cited by 3 - Related articles - All 9 versions

**[PDF]** Energy-Aware Opcode DesignFile Format: PDF/Adobe Acrobat - [View as HTML](#)**instruction-sets**. They tailor the **instruction-set** to the target .... To study the scalability of the **new opcode**-configuration, ...[iccd.et.tudelft.nl/2008/proceedings/570iyer.pdf](#) - Similar

by BV Iyer - Related articles

Run-Time Optimized Reconfiguration Using **Instruction** Forecastinga new category of processors, the dynamic **instruction set** processors (DISPs) ....**instruction**. Release processor from. HOLD state. Wait **new opcode** ...[www.springerlink.com/index/10t856qgkkrggwgu.pdf](#) - Similar

by M Iliopoulos - 2001 - Cited by 2 - Related articles

**Empirical Analysis of Mesa **Instruction Set**, 1982**The version of the Mesa **instruction set** under analysis .... On the other hand, each **new****opcode** adds complexity to the implementation. ...[www.oberon2005.ru/paper/rs1982-01e.pdf](#) - Similar

by RE Sweet - 1982 - Cited by 30 - Related articles

**[PDF]** Microsoft PowerPoint - hotchips5 [Read-Only]File Format: PDF/Adobe Acrobat - [View as HTML](#)**Instruction Set** Architecture .... **Instruction Set** Simulator. Function Libraries ... define a**new opcode** for byteswap opcode BYTESWAP op2=4'b0000 CUST0 ...[hotchips.org/archives/hc11/2\\_Mon/hc99.s4.3.Gonzalez.pdf](#) - Similar

by R Gonzalez - 1999 - Cited by 11 - Related articles

**[PDF]** Instruction Re-Encoding Facilitating Dense Embedded CodeFile Format: PDF/Adobe Acrobat - [View as HTML](#)the ISA (**Instruction Set** Architecture) is specified or not. When the ISA is specified, .... **new****opcode** must be unique for a specific application and ...[www.date-conference.com/archive/conference/..../06.7\\_4.PDF](#) - Similar

by T Bonny - Cited by 1 - Related articles

**[PDF]** Code CompressionFile Format: PDF/Adobe Acrobat - [View as HTML](#)**instruction set**. When we write base instructions in patter- .... tions that can be combinedby a **new opcode**-combined **instruction** pattern. ...[www.well.com/.../Ernst%20et%20al.%20Code%20compression.pdf](#) - Similar

by J Ernst - 1997 - Cited by 164 - Related articles - All 3 versions

**[PDF]** Instruction Sets: Ch 5

File Format: PDF/Adobe Acrobat - [View](#)

What is an **instruction set**? An **instruction set** specifies the machine-level (typically binary) ..... How could this be specified without a **new opcode**? ...  
[cobweb.ecn.purdue.edu/~ece495k/slides/ece495k-ch5.pdf](http://cobweb.ecn.purdue.edu/~ece495k/slides/ece495k-ch5.pdf) - Similar

### [PDF] Automatic Generation of Compact Programs and Virtual Machines for ...

File Format: PDF/Adobe Acrobat - [View](#)

erate a virtual **instruction set** capable of compact encoding of programs. .... minus the length of the **new opcode** multiplied by the fre- ...  
[repository.readscheme.org/ftp/papers/sw2000/latendresse.pdf](http://repository.readscheme.org/ftp/papers/sw2000/latendresse.pdf) - Similar

### [PDF] Annotated Memory References: A Mechanism for Informed Cache ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Their mechanism requires significant modification to the **instruction set** to ... Memory reference annotation can be implemented by adding a **new opcode** for ...  
[www.cs.duke.edu/~alvy/papers/ice-euro-long.pdf](http://www.cs.duke.edu/~alvy/papers/ice-euro-long.pdf) - Similar  
by AR Lebeck - 1999 - Cited by 2 - Related articles

### [PDF] Microsoft PowerPoint - ee675\_coprocessor\_interface

File Format: PDF/Adobe Acrobat - [View as HTML](#)

**instruction set** may be extended by the addition of up .... These conditions indicate that this cycle is an ARM **instruction** fetch so the **new opcode** ...  
[www.engineering.sdsu.edu/..../ee675\\_coprocessor\\_interface.pdf](http://www.engineering.sdsu.edu/..../ee675_coprocessor_interface.pdf) - Similar

### [PDF] Compressing MIPS Code by Multiple Operand Dependencies

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Additional Key Words and Phrases: Code compression, **instruction set** ..... assembly **instruction** (also the **new opcode** used in this paper) column shows all ...  
[ir.lib.nctu.edu.tw/bitstream/987654321/21662/1/020135005.pdf](http://ir.lib.nctu.edu.tw/bitstream/987654321/21662/1/020135005.pdf) - Similar  
by K Lin - 2003 - Cited by 2 - Related articles - All 4 versions

### [PDF] High Performance, Variable-Length **Instruction** Encodings Heidi Pan

File Format: PDF/Adobe Acrobat - [View](#)

compress a fixed-length **instruction set** and to use a variable-length **instruction set**. Com- ..... information encoded in the head as a **new opcode** ...  
[groups.csail.mit.edu/cag/scale/papers/xoxo-meng.pdf](http://groups.csail.mit.edu/cag/scale/papers/xoxo-meng.pdf) - Similar  
by H Pan - 2002 - Cited by 4 - Related articles

### [PDF] COPYRIGHTED MATERIAL

File Format: PDF/Adobe Acrobat - [View](#)

a **new opcode**. To change the argument, first click on the argument region of the selected ... It's too bad the SimCom **instruction set** ...  
[media.wiley.com/product\\_data/excerpt/00/.../0782141900.pdf](http://media.wiley.com/product_data/excerpt/00/.../0782141900.pdf) - Similar

### [PDF] An Extender's Guide to the Optimization Programming Interface and ...

File Format: PDF/Adobe Acrobat - [View](#)

different revisions of the same **instruction set** architecture (ISA), and you certainly don't want to do .... appropriate print method for each **new opcode** ...  
[www.eecs.harvard.edu/huber/software/nci/opi-extenders.pdf](http://www.eecs.harvard.edu/huber/software/nci/opi-extenders.pdf) - Similar  
by G Holloway - Cited by 4 - Related articles - All 6 versions

### [PDF] Microsoft PowerPoint - fogie DC 11 PPT

File Format: PDF/Adobe Acrobat - [View as HTML](#)

Reduced **Instruction Set** Computer (RISC). • ARM (1987): StrongARM, Xscale .... **New Opcode**. New Hex. 0x11294. 0x694. Cmp r0, #8. 08 00 50 E3. Cmp r0, r0 ...  
[www.airscanner.com/pubs/fogieDC11.pdf](http://www.airscanner.com/pubs/fogieDC11.pdf) - Similar

### [PDF] A Writable Computer

File Format: PDF/Adobe Acrobat - [View](#)

(writable **instruction set** computer), MISC (minimum **instruction set** computer), ..... **instruction** register sequence out to program memory to get a **new opcode** ...  
[clubweb.interbau.com/~rc/Papers/AWritableComputer.pdf](http://clubweb.interbau.com/~rc/Papers/AWritableComputer.pdf) - Similar

### [PDF] Compiler-Directed Early Load-Address Generation

File Format: PDF/Adobe Acrobat - [View](#)

"new opcode" instruction set filetype:pdf - Google Search

the **new opcode** indicating that the address can be pre- dicted is used, a hardware table is ... **instruction set** modification is minimal while the con- ... impact.crhc.illinois.edu/ftp/conference/micro-98-earlyload.pdf - Similar by BCCDA Connors - Related articles

### Optimizing direct threaded code by selective inlining

We introduced a **new opcode** class RELATIVE2 for these, which differs from. RELATIVE only .... cause its **instruction set** matches very closely the underlying ... lamp.epfl.ch/teaching/archive/advanced\_compiler/.. ./plumarta.pdf - Similar by I Plumarta - 1996 - Cited by 125 - Related articles - All 22 versions

### [PDF] Trimaran: A Compiler and Simulator for Research on Embedded and ...

File Format: PDF/Adobe Acrobat - View as HTML Trimaran generates and evaluates code for two **instruction set** architectures (ISAs). ..... Each **new opcode** requires an integer and a string value. .... www.trimaran.org/docs/trimaran4\_manual.pdf - Similar

### [PDF] Low-power Data Memory Communication for Application-Specific ...

File Format: PDF/Adobe Acrobat - View special loop support in the **instruction set**, conditional branches are used to form the loop. ... This **new opcode** signals to the CPSI to assert the addi- .... www.cecs.uci.edu/conference\_proceedings/.../petrov\_low-power.pdf - Similar by P Petrov - 2002 - Cited by 5 - Related articles - All 16 versions

### [PDF] Engineering a Common Intermediate Representation for the Ovm Framework

File Format: PDF/Adobe Acrobat - View as HTML nents by their **instruction set** using a common and intermediate ..... that may encounter the **new opcode**. For LDC DW, the only place that requires ... grothoff.org/christian/ecir.pdf - Similar by KPJBC Flack - Cited by 11 - Related articles - All 10 versions

### [PDF] Digital Circuits Pattern

File Format: PDF/Adobe Acrobat - View as HTML by composing the available **instruction set** following a specific order. .... define a **new opcode** for byteswap opcode opcode. BYTESWAP op2=4'b0000 ... parlab.eecs.berkeley.edu/wiki/\_media/patterns/digital\_circuits.pdf - Similar by CH Low - Related articles

### [PDF] Demystifying EPIC and IA-64: 1/26/98

File Format: PDF/Adobe Acrobat - View as HTML The **new opcode** indicates the remaining bits in the **instruction** provide additional encoding ... newer **instruction set**. In contrast, IA-64 binaries could not ... www.cs.cmu.edu/afs/cs/academic/.../mpr\_ia64\_demyst\_jan98.pdf - Similar by P Song - Cited by 11 - Related articles - All 6 versions

### [PDF] Automatic Generation of Compact Programs and Virtual Machines for ...

File Format: PDF/Adobe Acrobat - View erate a virtual **instruction set** capable of compact encoding of programs. These techniques are: ... This introduces a **new opcode**. And in most cases, the ar- .... www.iro.umontreal.ca/~latendre/publications/pli.pdf - Similar

### [PDF] W65C02S Microprocessor DATA SHEET

File Format: PDF/Adobe Acrobat - View as HTML Variable length **instruction set** provides for lower power and smaller code ..... **New OpCode**. 1. 1. 1. 1/0. 1. 16.. Zero Page Indirect Indexed with y ... homepage.mac.com/jorgechamorro/a2things/PDFs/65c02\_WDC.pdf - Similar

### [PDF] JavaCard— From Hype to Reality

File Format: PDF/Adobe Acrobat - View as HTML or **instruction set**, as this was still under discussion throughout 1998. Such ques- ..... **new opcode** that loads a field of the ... www.zurich.ibm.com/pdf/javacard.pdf - Similar by M Computing - Related articles

### [PDF] Object-Oriented ASIP Design and Synthesis

File Format: PDF/Adobe Acrobat - [View](#)

class library into an ASIP (application-specific **instruction set** processor) .... the "hardware class library" and assigning a **new opcode** to each first-ever ...

[www.cl.cam.ac.uk/~am21/papers/fdi03.pdf](http://www.cl.cam.ac.uk/~am21/papers/fdi03.pdf) - [Similar](#)

by M Goudarzi - Cited by 14 - Related articles - All 6 versions

[PS] [Improving Indirect Branch Prediction With Source- and Arity-based ...](#)

File Format: Adobe PostScript - [View as HTML](#)

plus static linking) and run under the shade **instruction**-level simulator [CK93] to .... would require the introduction of a **new opcode** or special register usage ..... Shade: A Fast

**Instruction-Set Simulator** for Execution Profiling. ....

[reference.kfupm.edu.sa/.../improving\\_indirect\\_branch\\_prediction\\_wit\\_1809850.pdf](http://reference.kfupm.edu.sa/.../improving_indirect_branch_prediction_wit_1809850.pdf) - [Similar](#)

by K Driesen - Cited by 9 - Related articles - All 8 versions

[1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#)

[Next](#)

---

[Search within results](#) - [Language Tools](#) - [Search Help](#) - [Dissatisfied? Help us improve](#) - [Try Go](#)

---

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [Privacy](#) - [About Google](#)